

Refine Search

Search Results -

Terms	Documents
L7 and (multiplexer or mux)	33

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L8

Search History

DATE: Thursday, September 22, 2005 [Printable Copy](#) [Create Case](#)

Set Name **Query**
 side by side

Hit Count **Set Name**
 result set

DB=USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L8</u>	L7 and (multiplexer or mux)	33	<u>L8</u>
<u>L7</u>	L6 and l4	49	<u>L7</u>
<u>L6</u>	interface or gasket	986305	<u>L6</u>
<u>L5</u>	L2 same fpga	28	<u>L5</u>
<u>L4</u>	L1 same fpga	62	<u>L4</u>
<u>L3</u>	L2 same l1	23	<u>L3</u>
<u>L2</u>	embedded adj1 (core or device)	2951	<u>L2</u>
<u>L1</u>	scan adj1 chain	2170	<u>L1</u>

END OF SEARCH HISTORY